IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant:	**Jean-Marc Bachot, et al.**	Docket:	**TI-27700A**
Serial No.:	**TBD**	Art Unit:	**TBD**
Filed:	**Herewith**	Examiner:	**TBD**
For:	**Method And Apparatus For Accessing A**	Conf. No.:	**7423**
	Memory Core Multiple Times In A Single		
	Clock Cycle		

INFORMATION DISCLOSURE STATEMENT

Assistant Commissioner For Patents

Alexandria, VA 22313-1450

Dear Sir:
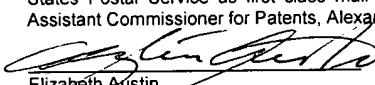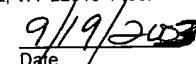
Applicant wishes to bring to the attention of the Patent and Trademark Office the information noted on the enclosed PTO-1449.

The group of references listed in the *Other Documents* section, reference numbers CA-CY, are all co-related and co-filed on the same date of October 1, 1999, claiming priority to their European first-filed counterparts.

Copies of the noted references are enclosed herewith, except references CA-CY, the co-related cases.

This information disclosure statement is being mailed before receipt of a first office action on the merits in accordance with 37 C.F.R. 1.97(b)(3), therefore, no fees are due.

Respectfully submitted,
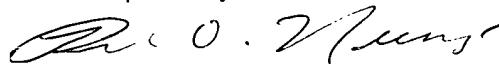
Ronald O. Neerings

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Substitute for Form 1449A/PTO				Complete If Known	
INFORMATION DISCLOSURE STATEMENT BY APPLICANT (use as many sheets as necessary)				Application Number	*TBD*
				Filing Date	*Herewith*
				First Named Inventor	*Jean-Marc Bachot, et al.*
				Group Art Unit	*TBD*
				Examiner Name	*TBD*
Sheet	*1*	of	*2*	Attorney Docket No.	*TI-27700A*

U.S. PATENT DOCUMENTS						
Exam. Initials *	Cite No.[1]	U.S. Patent Document		Name of Patentee or Applicant of Cited Doc.	Date of Pub. of Cited Doc. (mm-dd-yy)	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number	Kind Code[2] (if known)			
	AA	5,414,751		Yamada	05/09/1995	
	AB	5,471,607		Garde	11/28/1995	
	AC	5,699,530		Rust, et al.	12/16/1997	
	AD	5,708,850		Staros	01/13/1998	
	AE	5,765,218		Ozawa, et al.	06/09/1998	
	AF	5,781,480		Nogle, et al.	07/14/1998	
	AG	5,790,443		Shen, et al.	08/04/1998	
	AH	5,896,543		Garde	04/20/1999	
	AI	5,923,615		Leach, et al.	07/13/1999	
	AJ	5,973,955		Nogle, et al.	10/26/1999	
	AK	6,078,527		Roth, et al.	06/20/2000	
	AL					
	AM					
	AN					
	AO					

FOREIGN PATENT DOCUMENTS							
Exam. Initials*	Cite No.[1]	Foreign Patent Document			Name of Patentee or Applicant of Cited Doc.	Date of Pub. of Cited Doc. (mm-dd-yy)	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T[6]
		Office[3]	Number[4]	Kind Code[2] (if known)			
	BA		EP 1 031 988 A1		Texas Instruments	30/08/2000		
	BB						
	BC						
	BD						
	BE						
	BF						
	BG						
	BH						
	BI						
	BJ						
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	BL						

*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

[1]Unique citation designation number. [2]See attached Kinds of U.S. Patent Documents. [3]Enter Office that issued the document, by the two-letter code (WIPO Standard ST.3). [4]For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. [5]Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST. 16 if possible. [6]Applicant is to place a check mark here if English language Translation is attached.

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Substitute for Form 1449A/PTO	Complete If Known	
	Application Number	TBD
INFORMATION DISCLOSURE	Filing Date	Herewith
STATEMENT BY APPLICANT	First Named Inventor	Jean-Marc Bachot, et al.
	Group Art Unit	TBD
(use as many sheets as necessary)	Examiner Name	TBD

Sheet	2	of	2	Attorney Docket No.	TI-27700A

OTHER PRIOR ART - NON PATENT LITERATURE DOCUMENTS			
Exam. Initials*	Cite No.[1]	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T[2]
	CA	TI-27677, A Bit Field Processor, co-filed as a regular application October 1, 1999	
	CB	TI-27678, Rounding Mechanisms in Processors, co-filed as a regular application October 1, 1999	
	CC	TI-27679, Linear Vector Computation, co-filed as a regular application October 1, 1999	
	CD	TI-27680, Hardware Accelerator/Acceleration for Processing Systems, co-filed as a regular application October 1, 1999	
	CE	TI-27681, Pipeline Protection, co-filed as a regular application October 1, 1999	
	CF	TI-27682, Pipelined Hardware Stack, co-filed as a regular application October 1, 1999	
	CG	TI-27683, A Processor With Conditional Execution of an Instruction Pair, co-filed as a regular application October 1, 1999	
	CH	TI-27684, A Processor With Local Instruction Looping, co-filed as a regular application October 1, 1999	
	CI	TI-27685, Compound Memory Access Instructions, co-filed as a regular application October 1, 1999	
	CJ	TI-27686, A Processor With a Computed Repeat Instruction, co-filed as a regular application October 1, 1999	
	CK	TI-27688, A Processor With Apparatus for Verifying Instruction Parallelism, co-filed as a regular application October 1, 1999	
	CL	TI-27689, Cache Miss Benchmarking, co-filed as a regular application October 1, 1999	
	CM	TI-27690, A Processor With Apparatus for Indexed Branch During Instruction Iteration, co-filed as a regular application October 1, 1999	
	CN	TI-27691, Circular Buffer Management, co-filed as a regular application October 1, 1999	
	CO	TI-27700, Method and Apparatus for Accessing a Memory Core Multiple Times in a Single Cl9ock Cycle, co-filed as a regular application October 1, 1999	
	CP	TI-27757, Improved Multiplier Accumulator Circuits, co-filed as a regular application October 1, 1999	
	CQ	TI-27758, Zero Anticipation Method and Apparatus, co-filed as a regular application October 1, 1999	
	CR	TI-27759, Trace FIFO Management, co-filed as a regular application October 1, 1999	
	CS	TI-27760, Stack Pointer Management, co-filed as a regular application October 1, 1999	
	CT	TI-27761, Software Breakpoint in a Delay Slot, co-filed as a regular application October 1, 1999	
	CU	TI-27762, Cache Coherence During Emulation, co-filed as a regular application October 1, 1999	
	CV	TI-27763, Memory Access Using Byte Qualifiers, co-filed as a regular application October 1, 1999	
	CW	TI-27764, Dual Interrupt Vector Mapping, co-filed as a regular application October 1, 1999	
	CX	TI-28234, Pipeline Protection, co-filed as a regular application October 1, 1999	
	CY	TI-28433, Improvements in or Relating to Microprocessors, co-filed as a regular application October 1, 1999	
	C1	"An Enhanced 16K E^{2}PROM", Lubin Gee, et al., IEEE Journal of Solid-State Circuits, Vol. SC-17, No. 5, 10/1982, pp. 828-832	
	C2	"Considerations for Selecting A DSP Processor (ADSP-2101 vs. TMS320C50)", B. Fine, et al., Microprocessors and Microsystems, Vol. 18, No. 6, 07/1994, pp. 351-362, XP000460483	
	C3	"Control Logic and Cell Design for a 4K NVRAM", Douglas J. Lee, et al., IEEE Journal of Solid-State Circuits, Vol. SC-18, No. 5, 10/1983, pp. 525-532	
	C4	"The Motorola DSP56000 Digital Signal Processor", Kevin L. Kloker, IEEE Micro, Vol. 6, No. 6, 12/1986, pp. 29-48, XP000211994	

Examiner Signature		Date Considered	

*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

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